

System And Method For Real Time Monitoring And  
Control Of Networked Computers

Inventor: Robert F. Terry

Serial No.: 09/827,891

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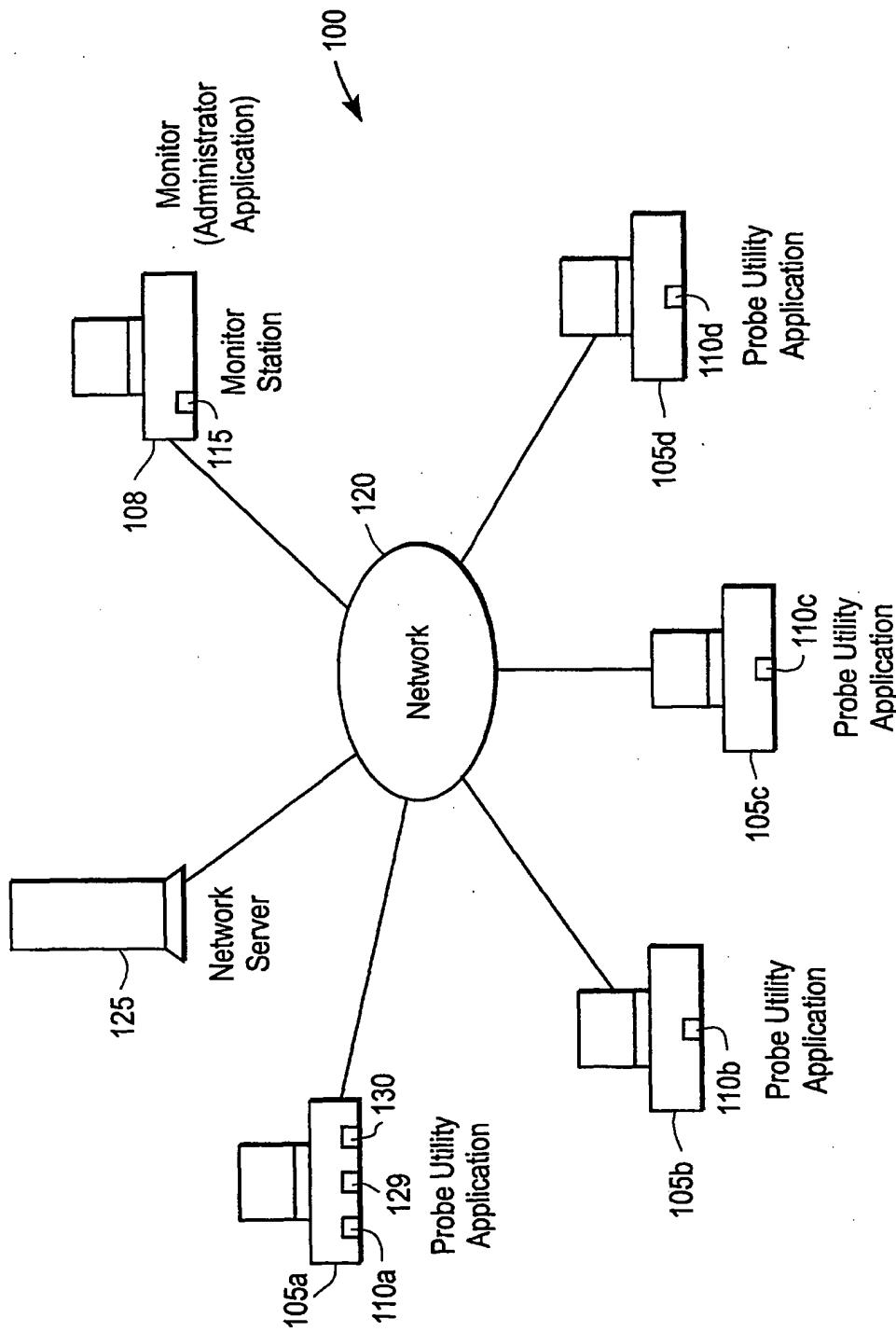


FIG. 1

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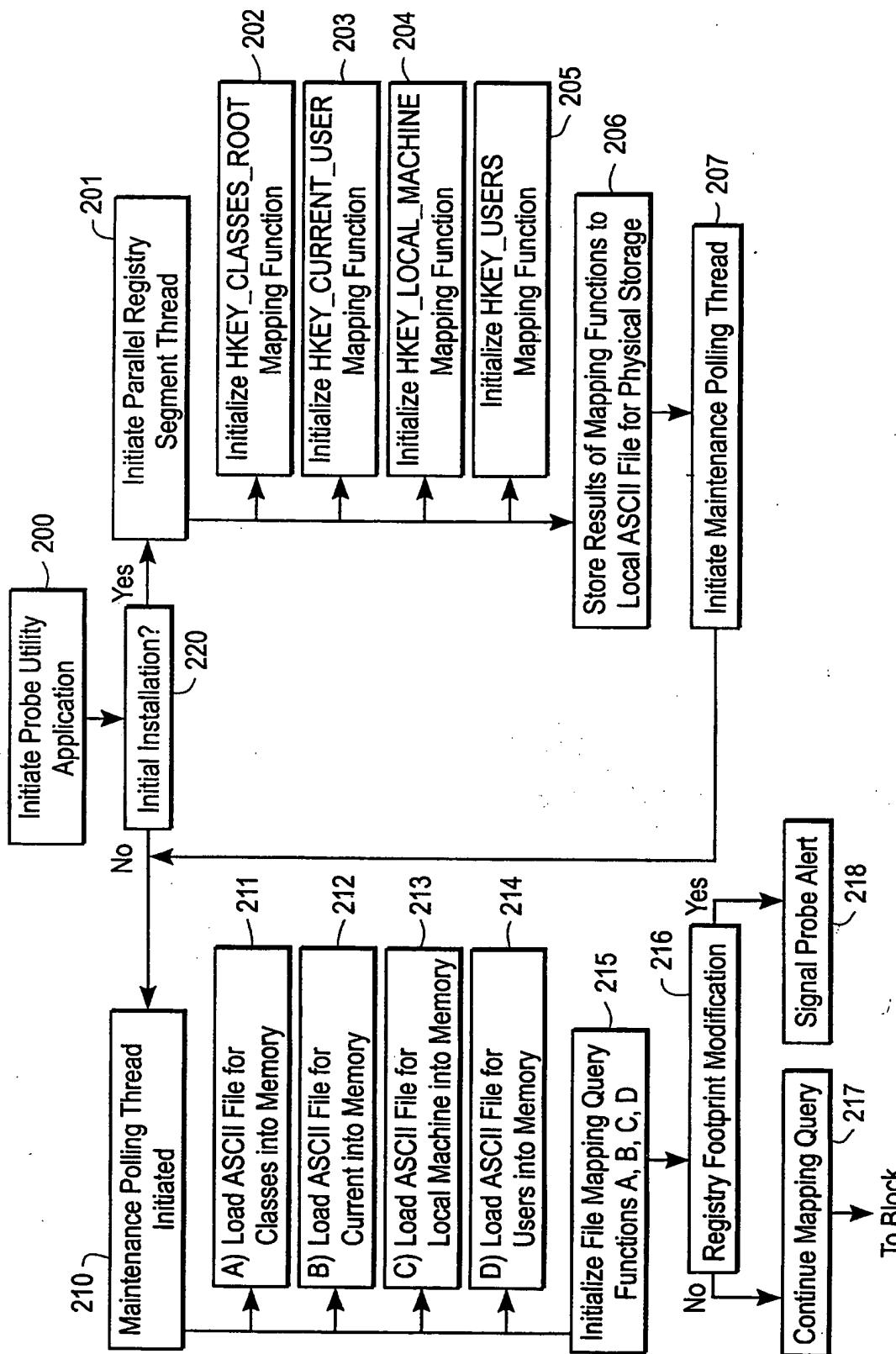


FIG. 2

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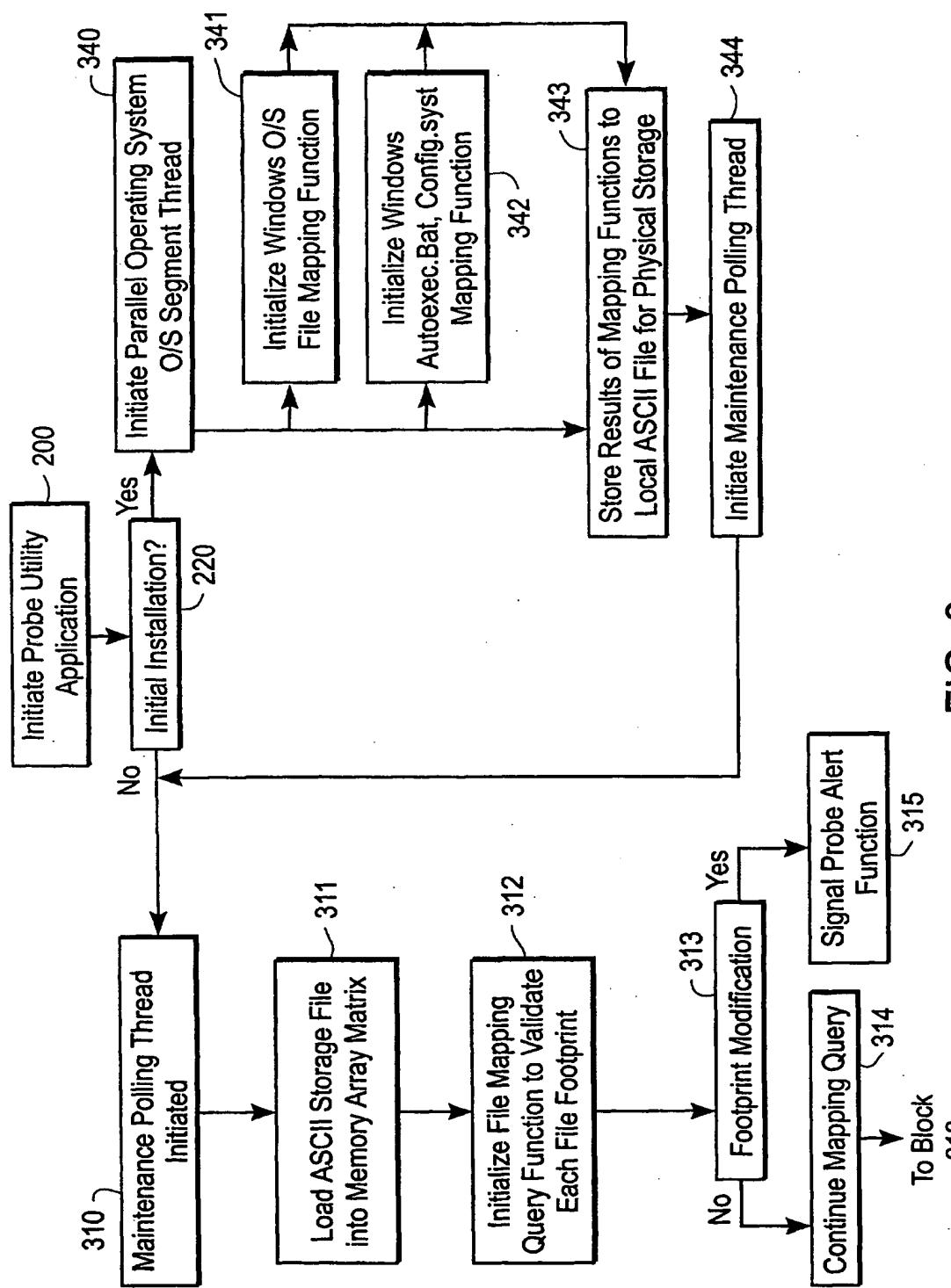


FIG. 3

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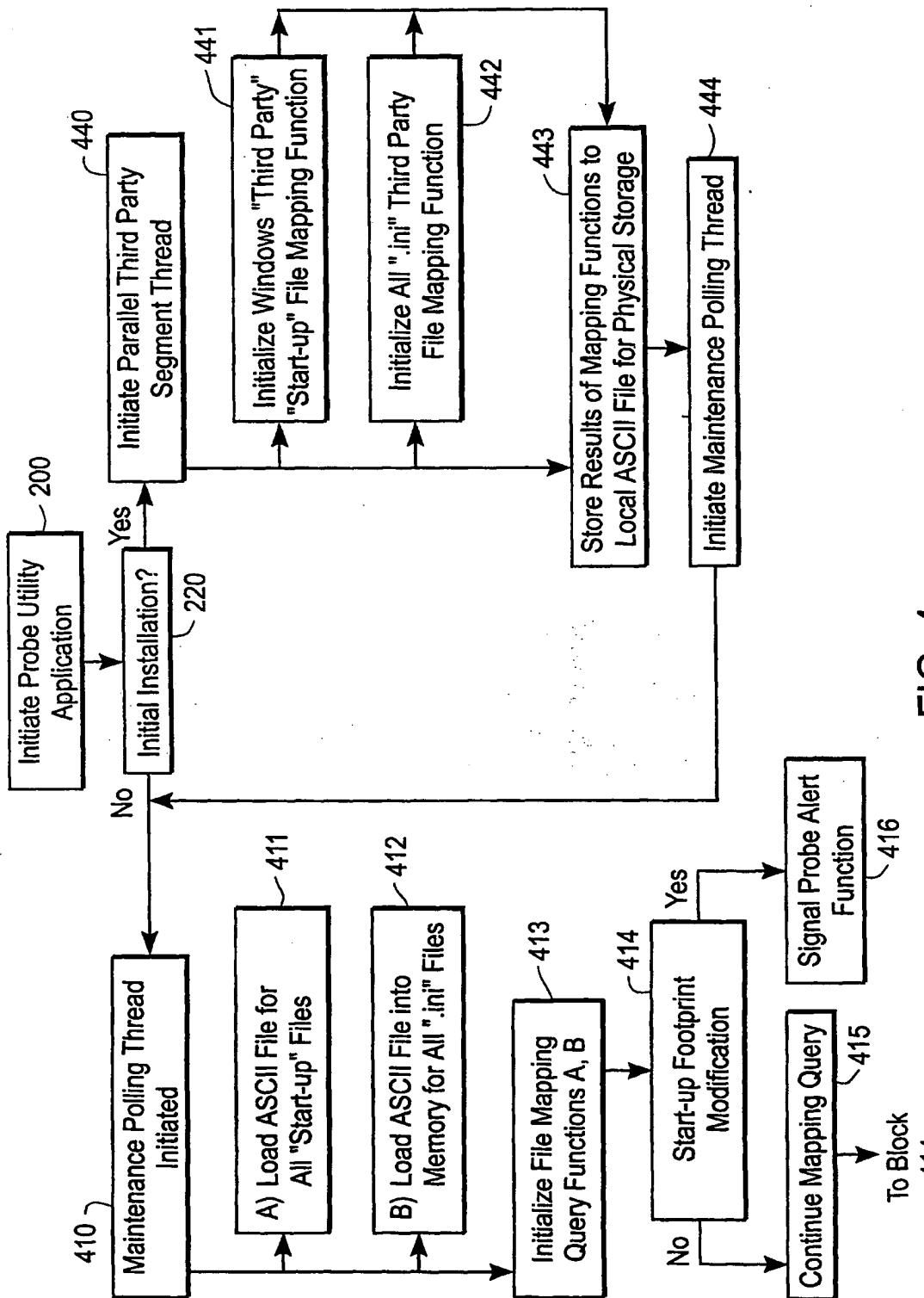


FIG. 4

*System And Method For Real Time Monitoring And  
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Inventor: Robert F. Terry

Serial No.: 09/827,891

102240-T684Z850

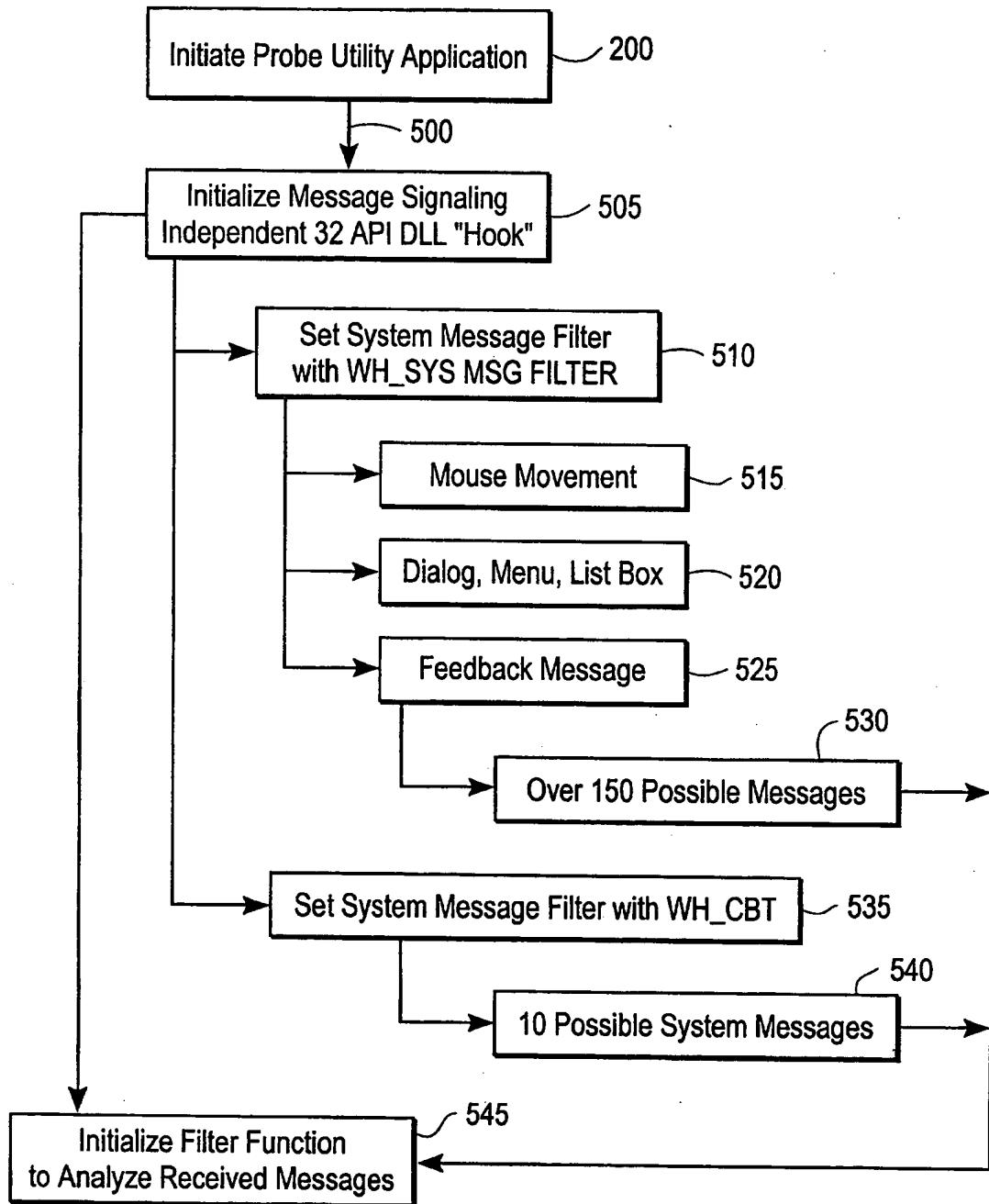


FIG. 5

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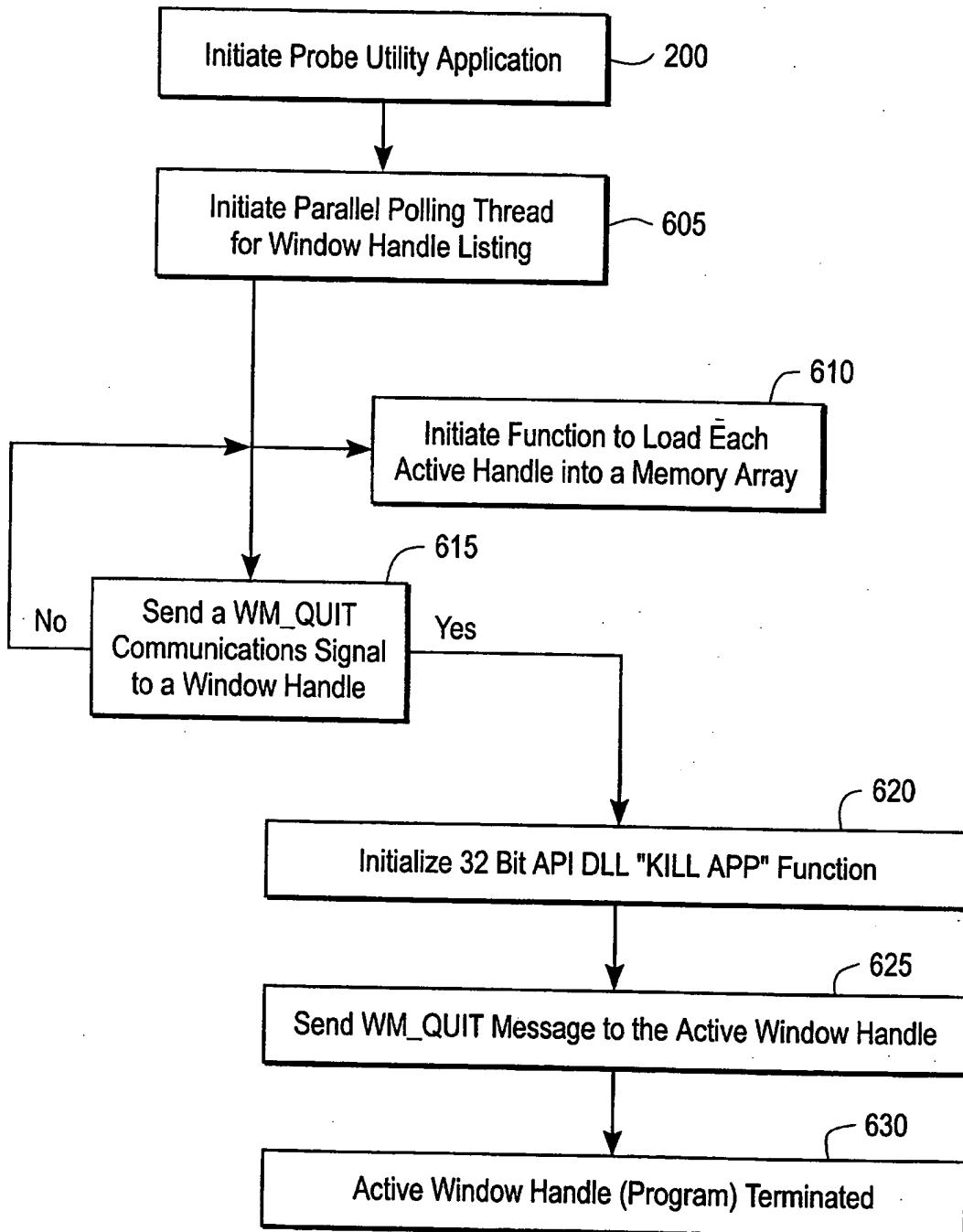


FIG. 6

System And Method For Real Time Monitoring And  
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Inventor: Robert F. Terry

Serial No.: 09/827,891

10242200-T682X2B60

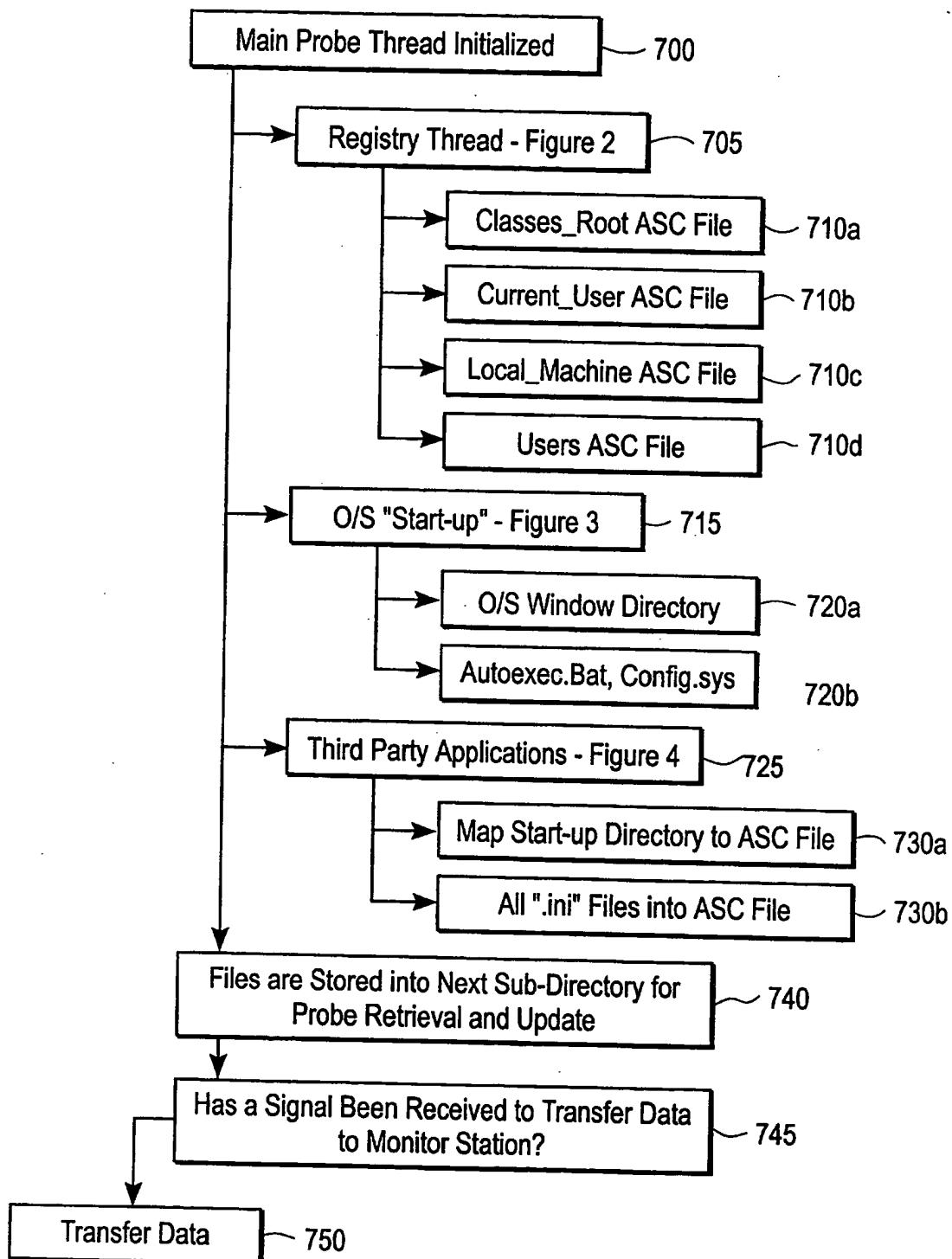


FIG. 7

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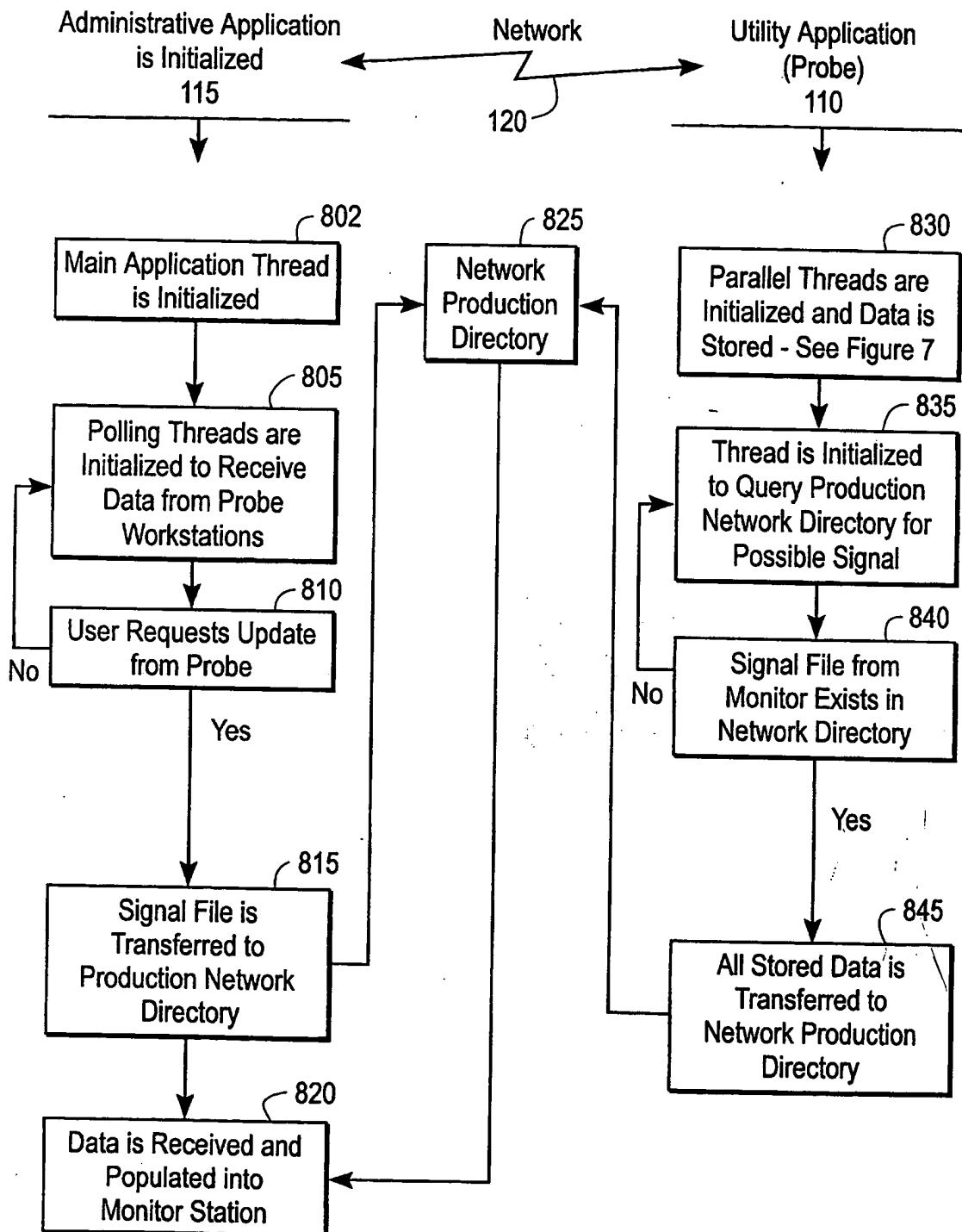


FIG. 8

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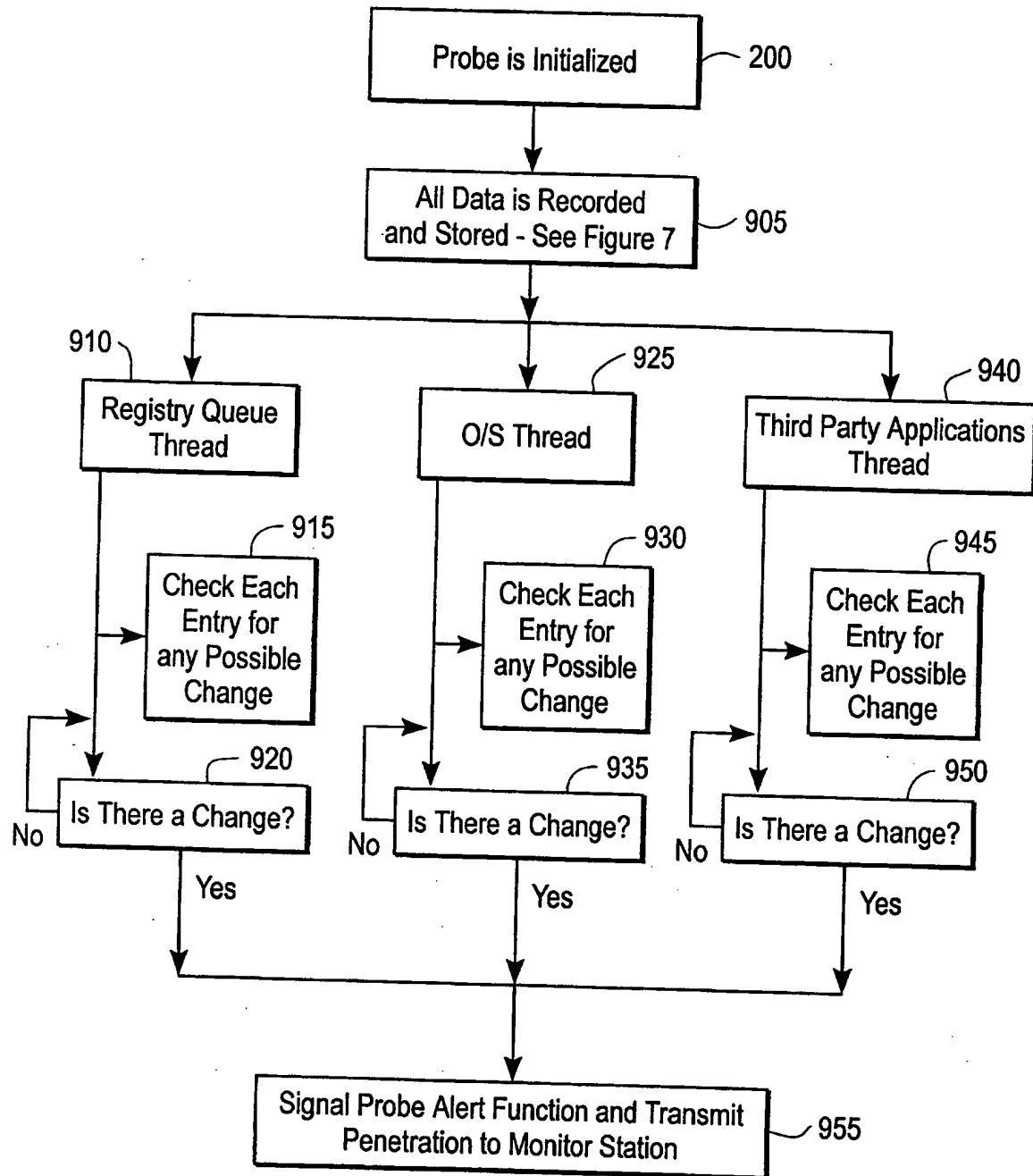


FIG. 9

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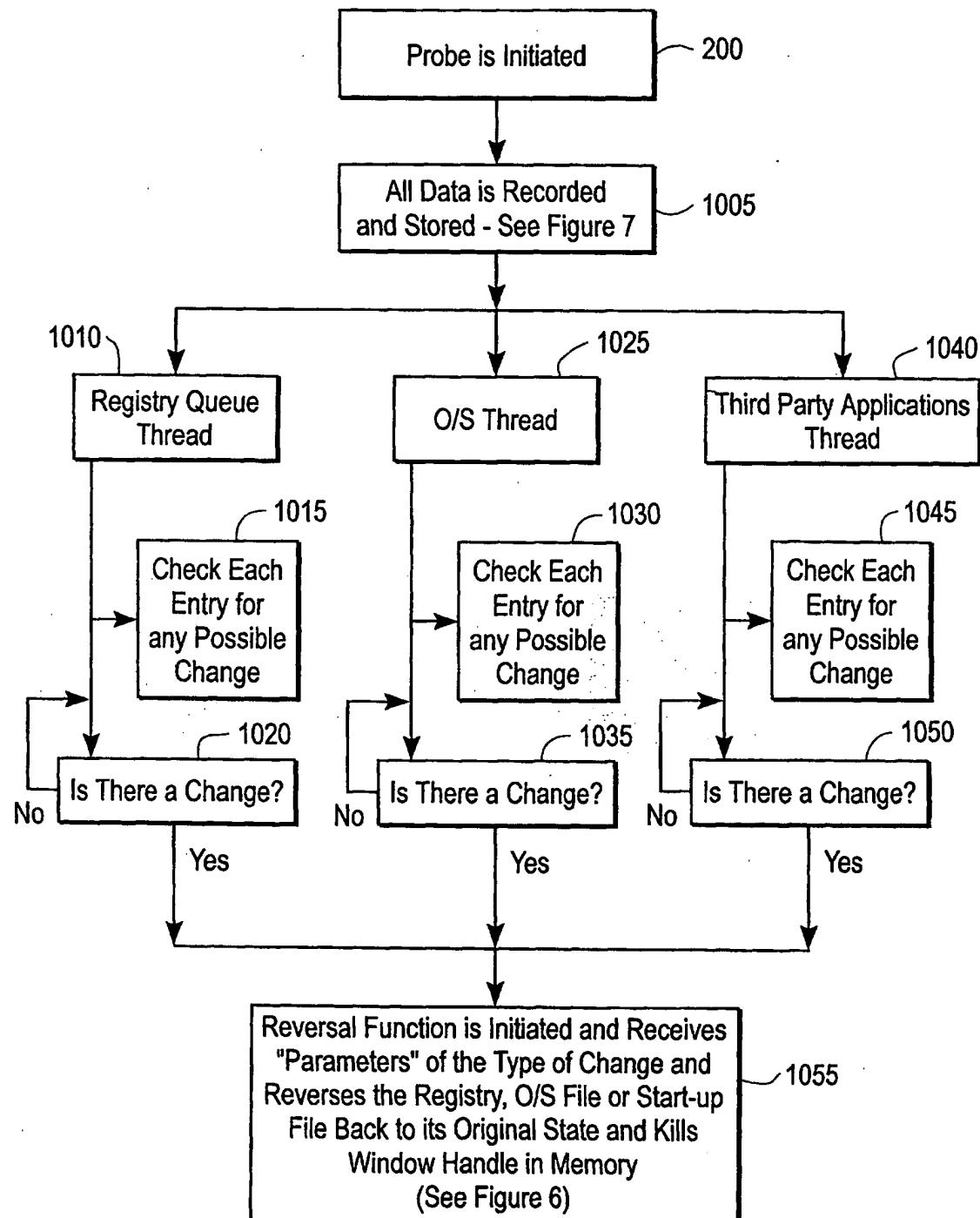


FIG. 10

SOT [CR] [LF]  
Date=CCYY\MM\DD [cr] [lf]  
Time=HH : MM : SS [cr] [lf]  
3Wind=Variable Up To 500 Characters [cr] [lf]  
2Wind=Variable Up To 500 Characters [cr] [lf]  
1Wind=Variable Up To 500 Characters [cr] [lf]  
Mssg=Variable Up To 500 Characters [cr] [lf]  
EOT [cr] [lf]

## Structured Signal File Block Diagram

FIG. 11

# Best Available Copy

TOECD "T6822860

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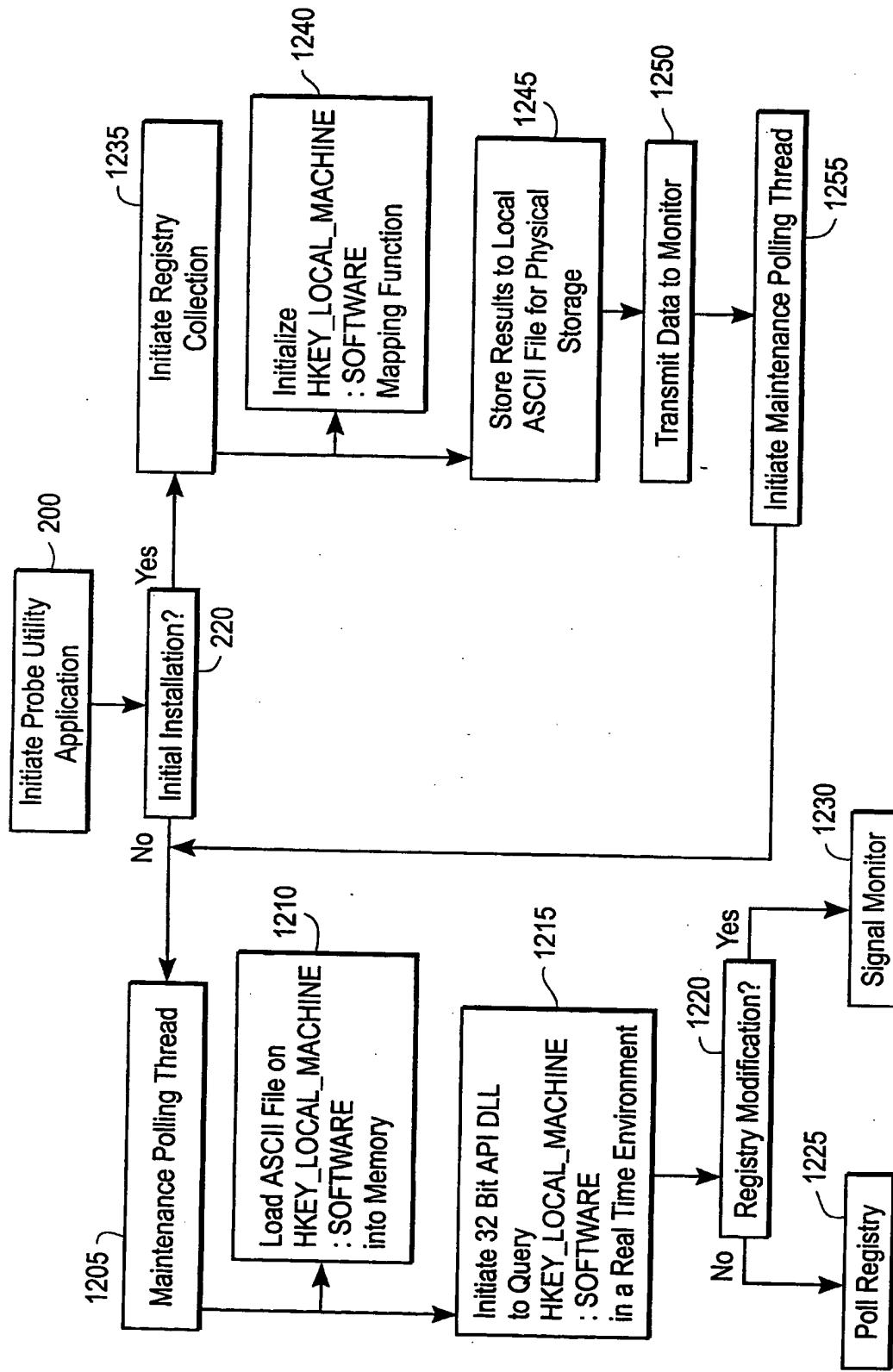


FIG. 12

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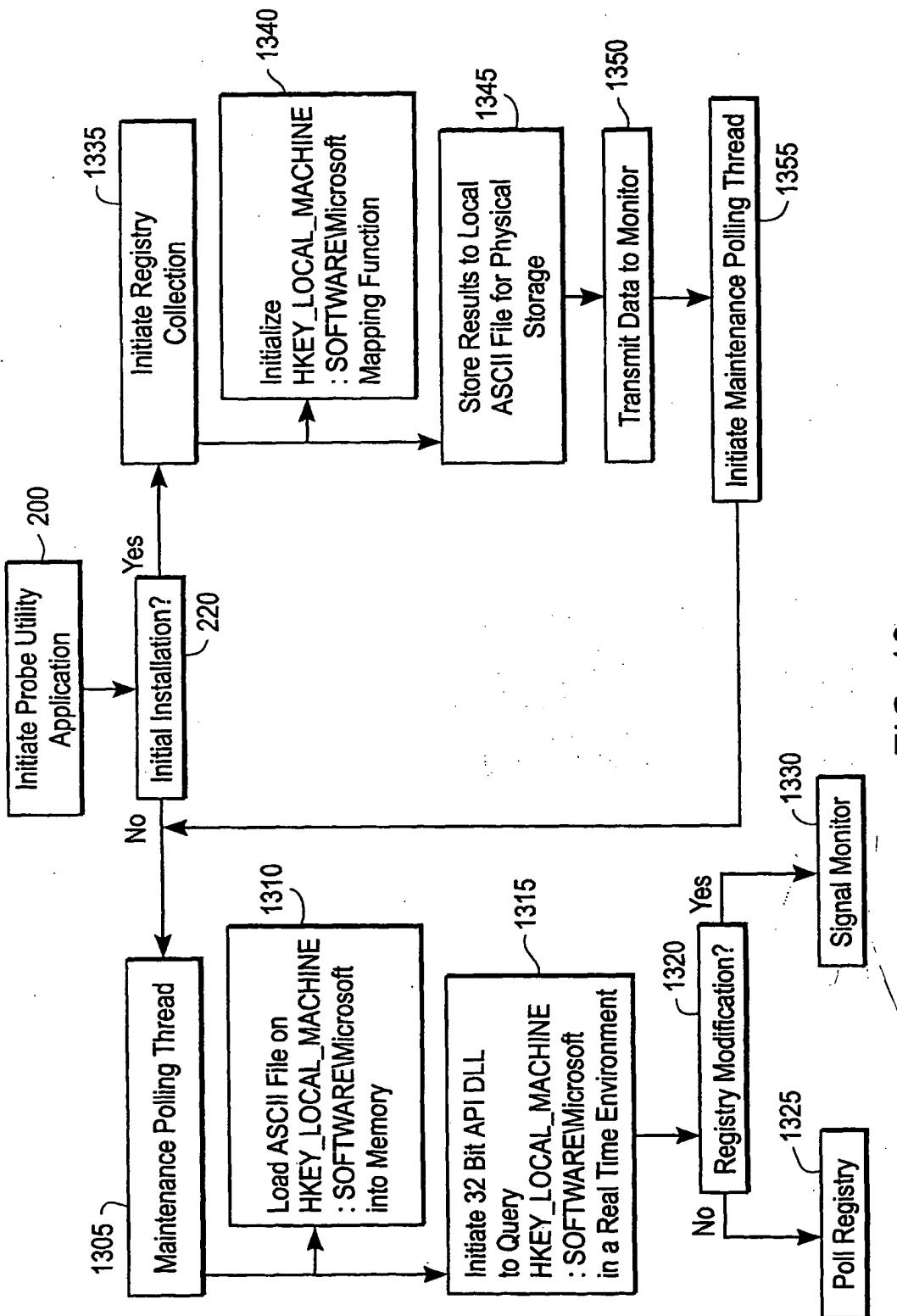


FIG. 13

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Inventor: Robert F. Terry

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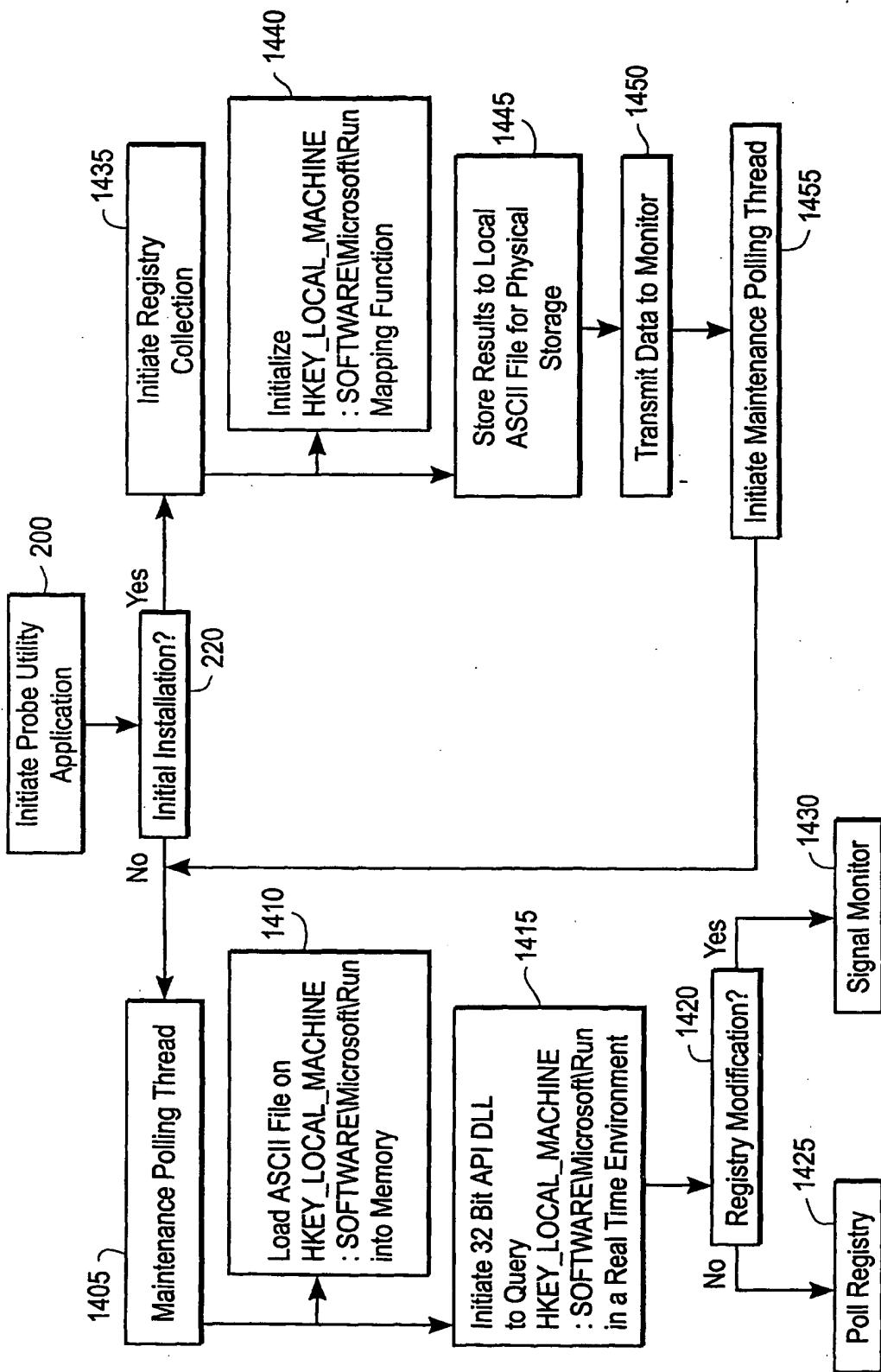


FIG. 14

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702240 " 75022860

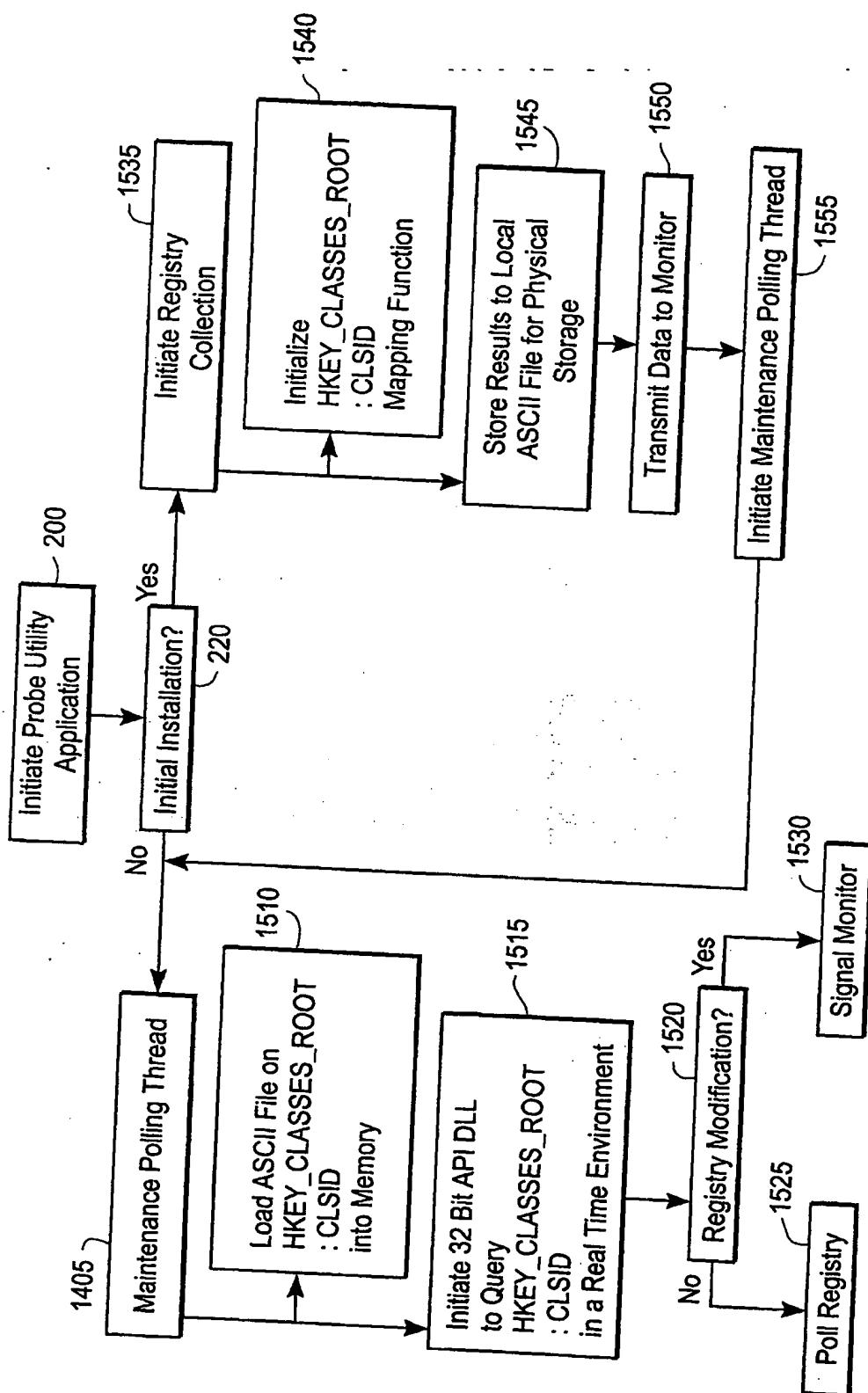


FIG. 15

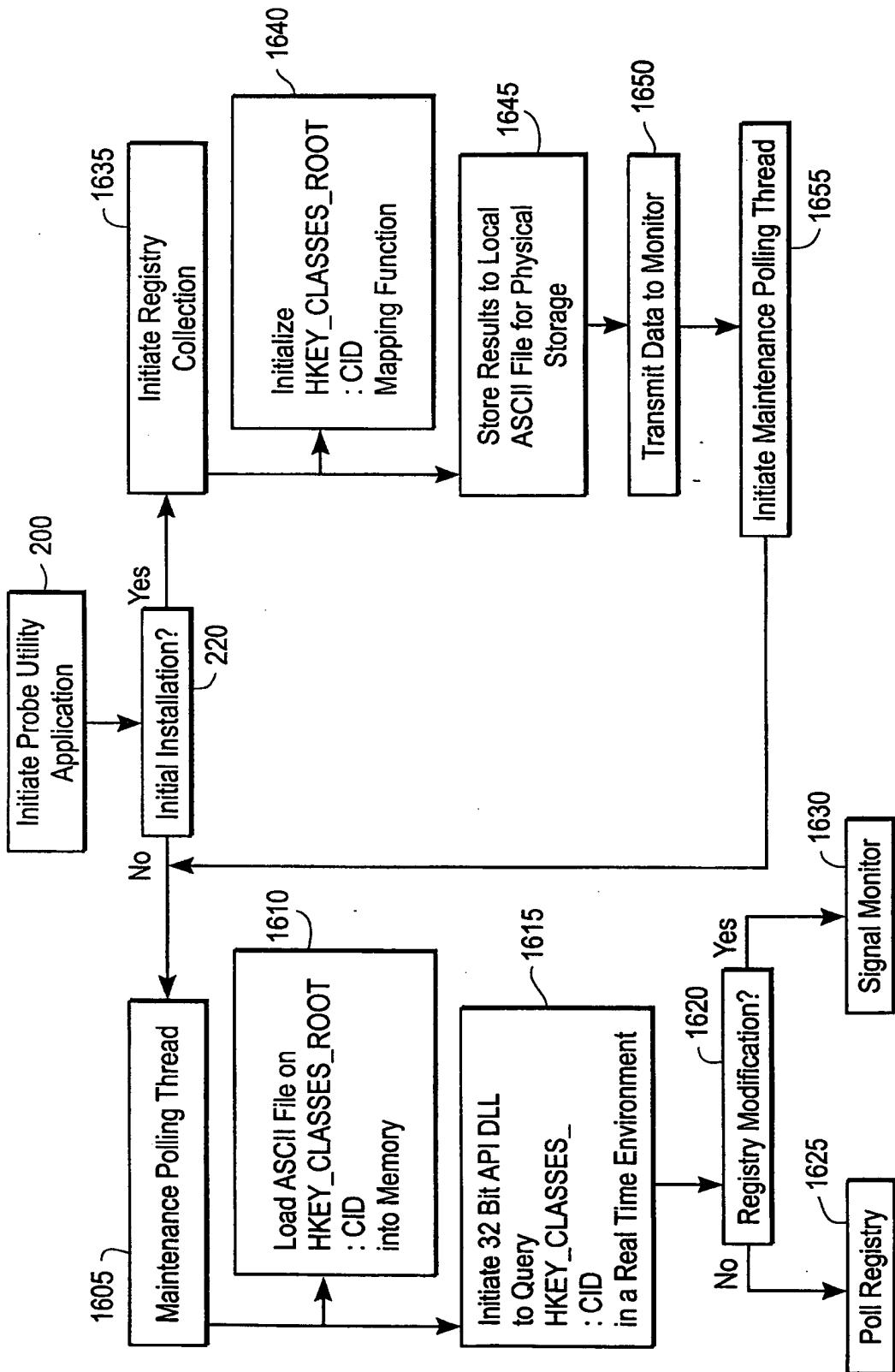


FIG. 16

TECHNICAL FIELD

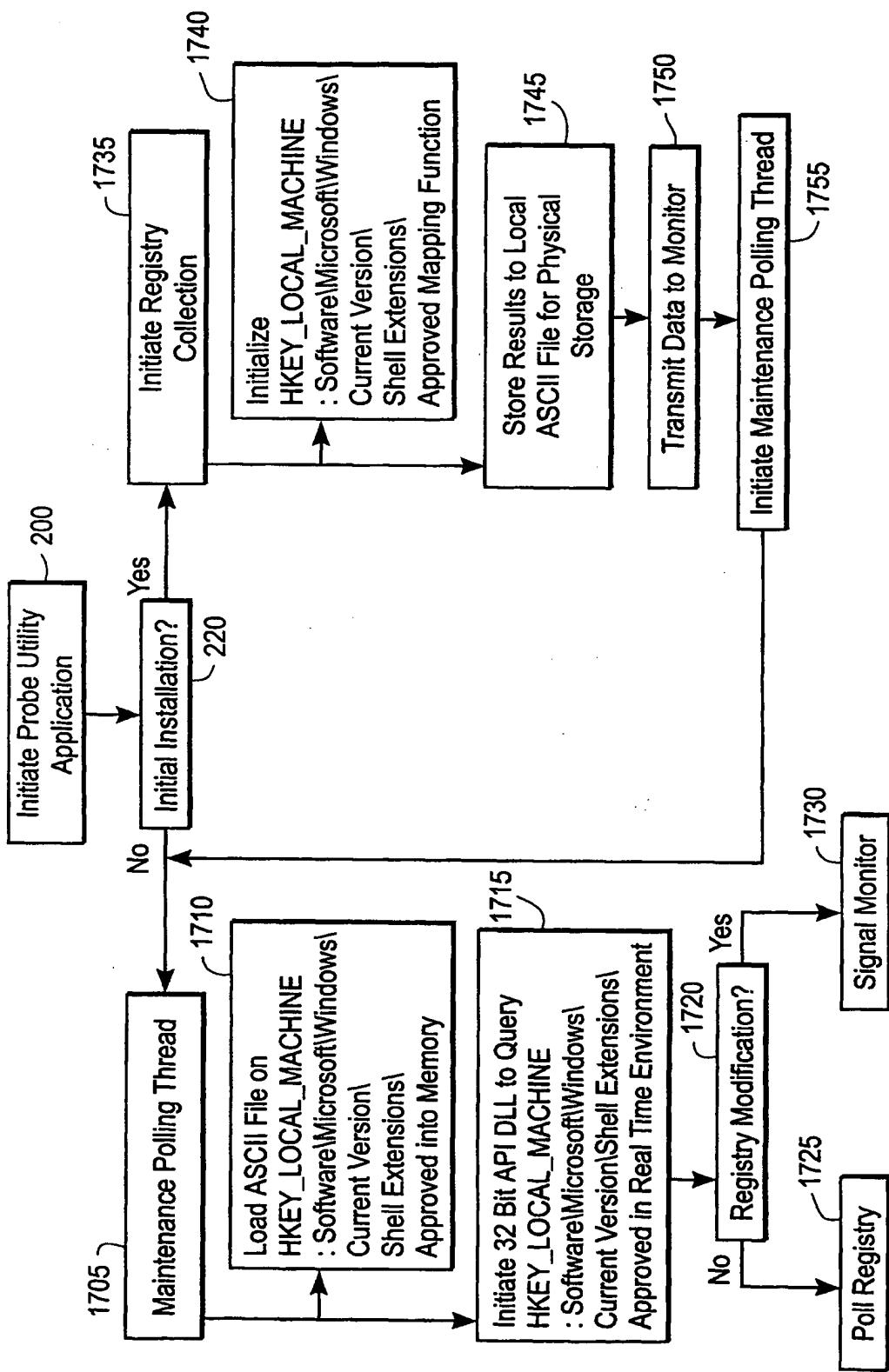


FIG. 17

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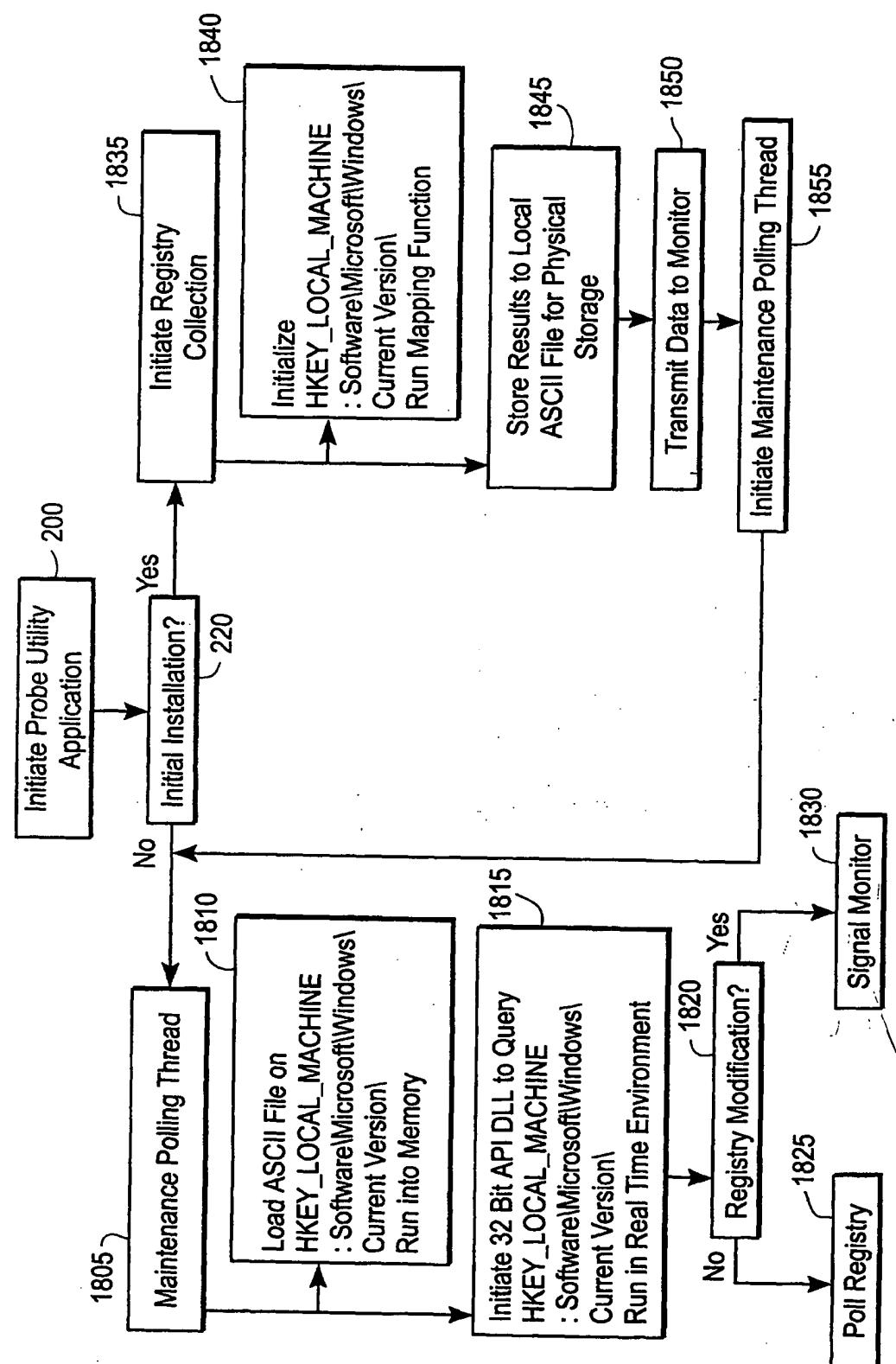


FIG. 18

Best Available Copy

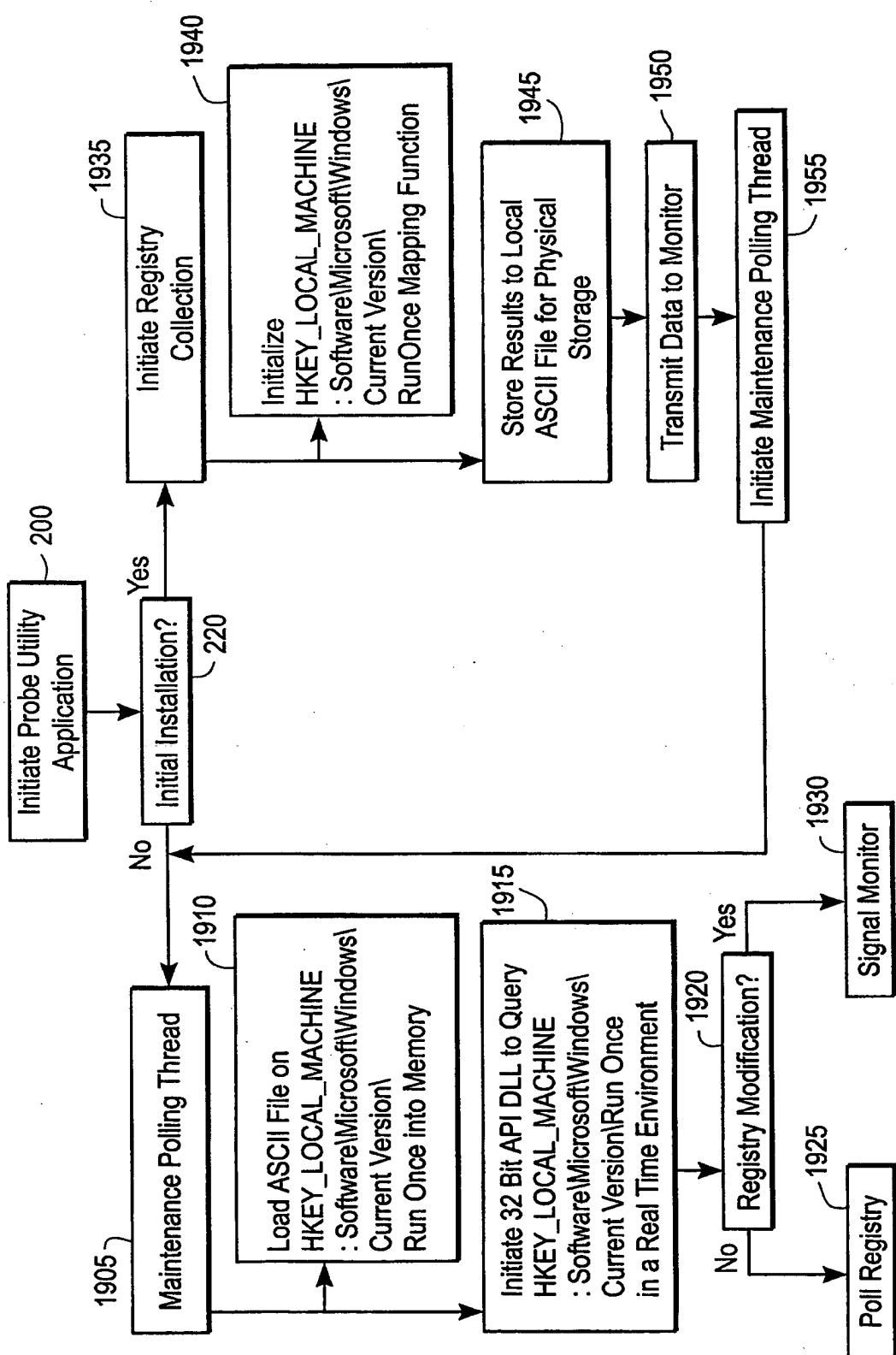


FIG. 19

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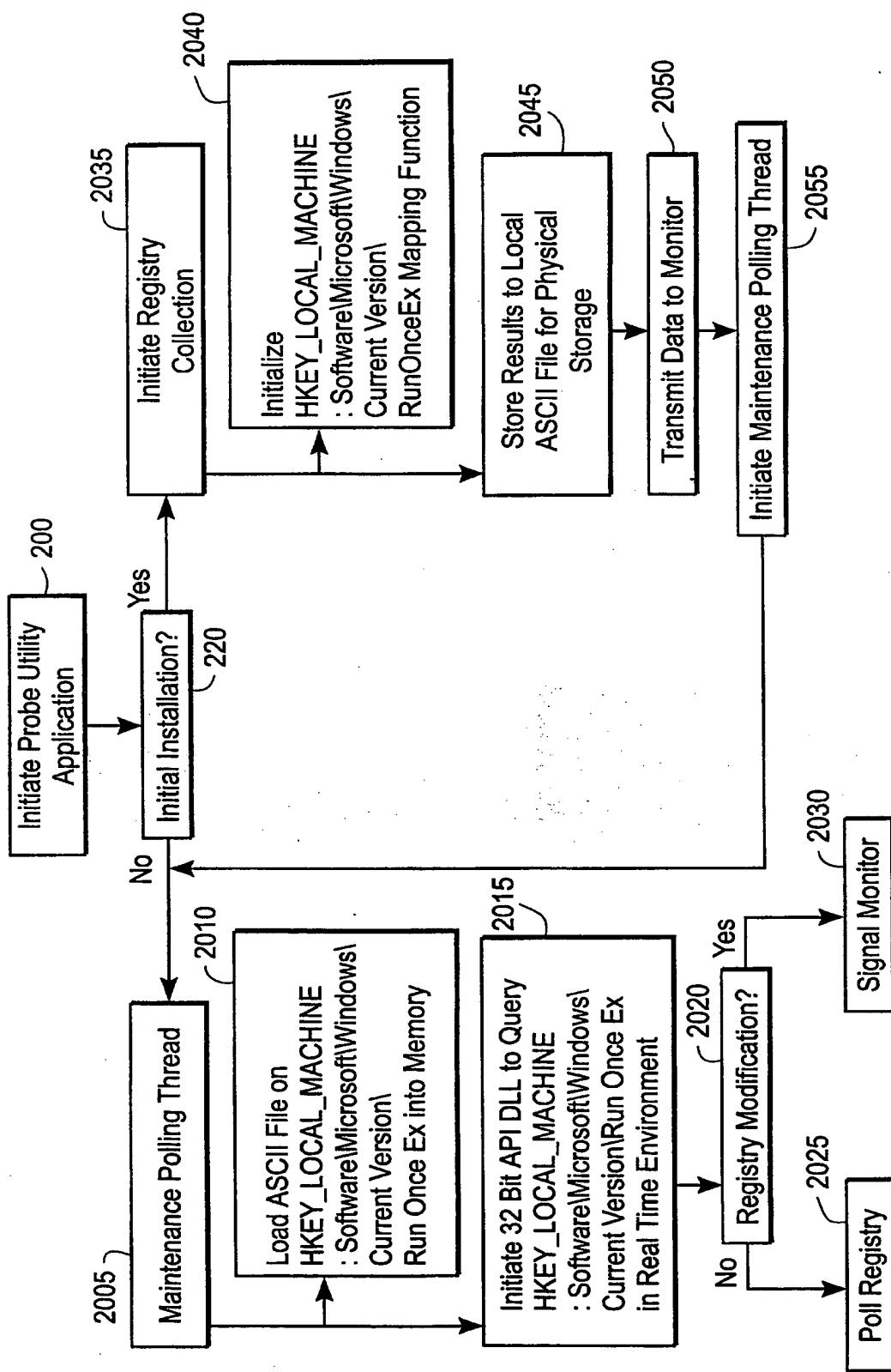


FIG. 20

System And Method For Real Time Monitoring and Control Of Networked Computers

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70222075228150

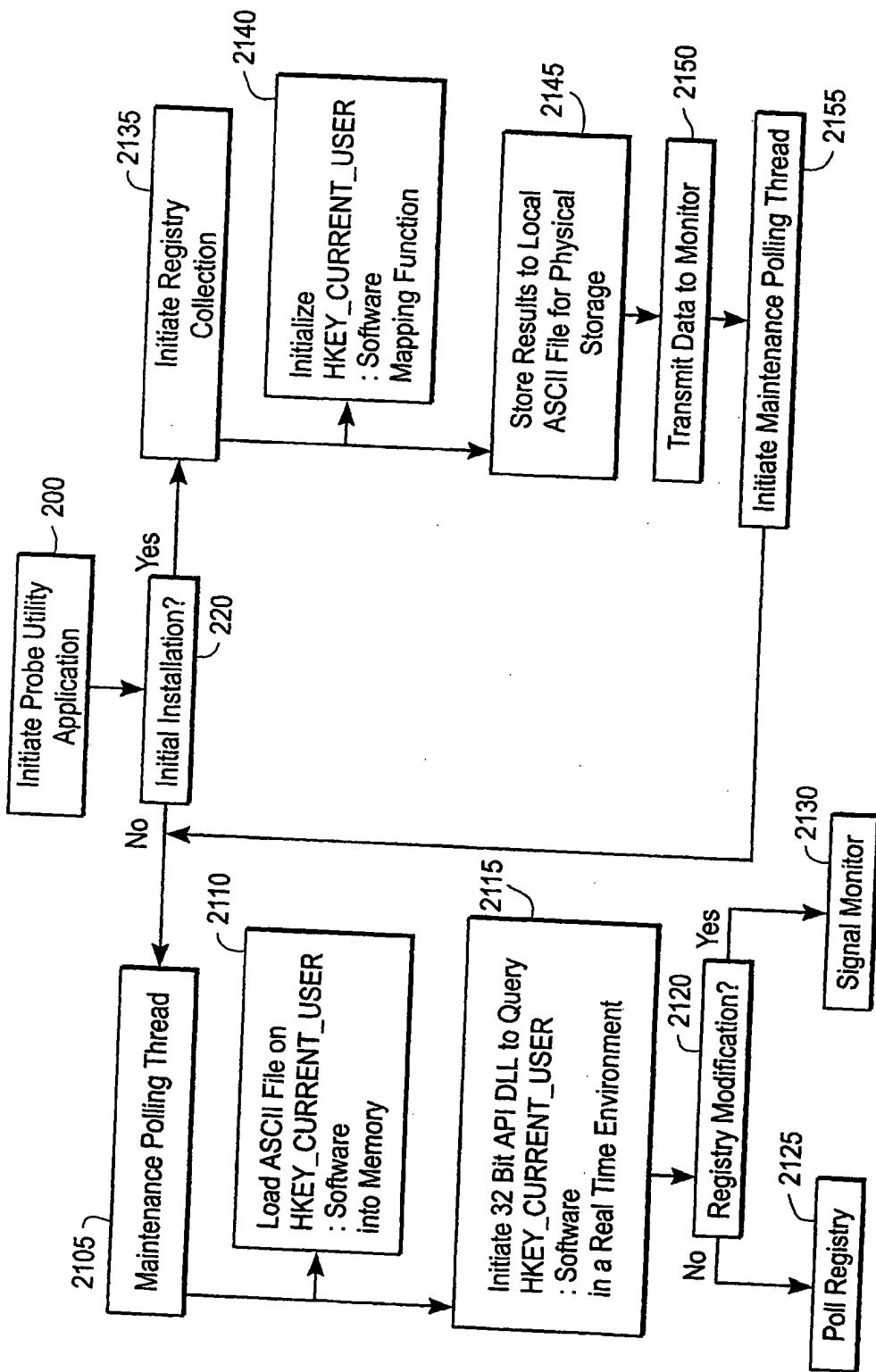
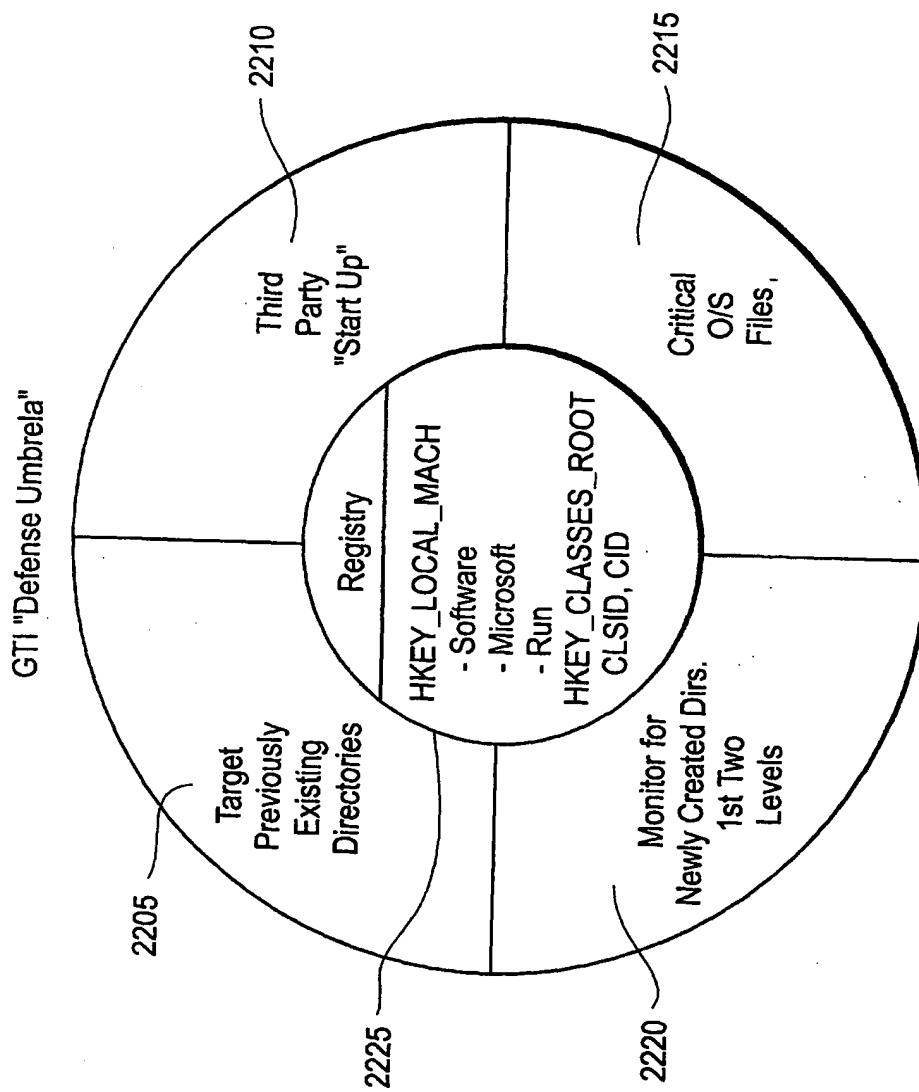


FIG. 21

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"Guarded State" of the Complete PC

FIG. 22

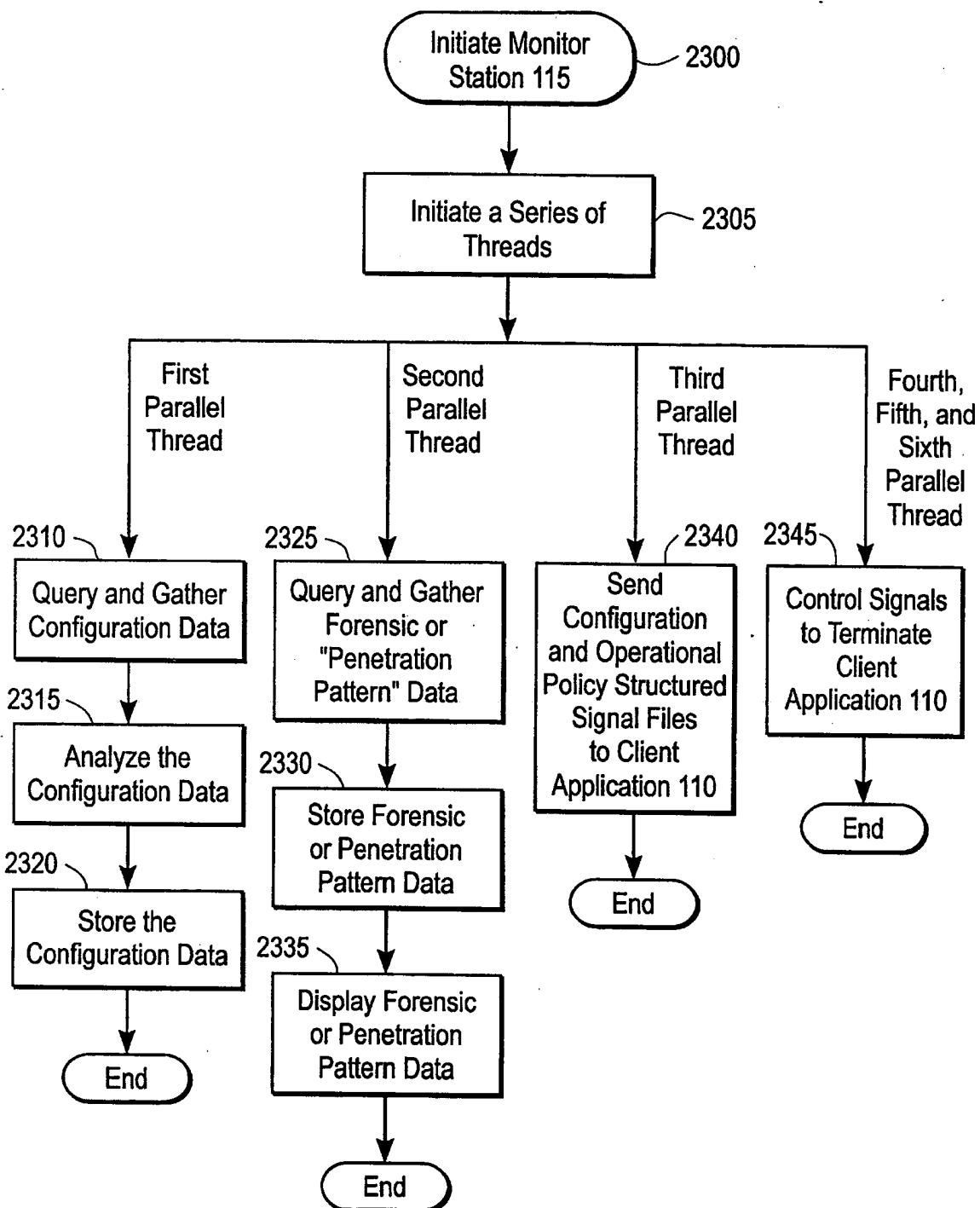


FIG. 23

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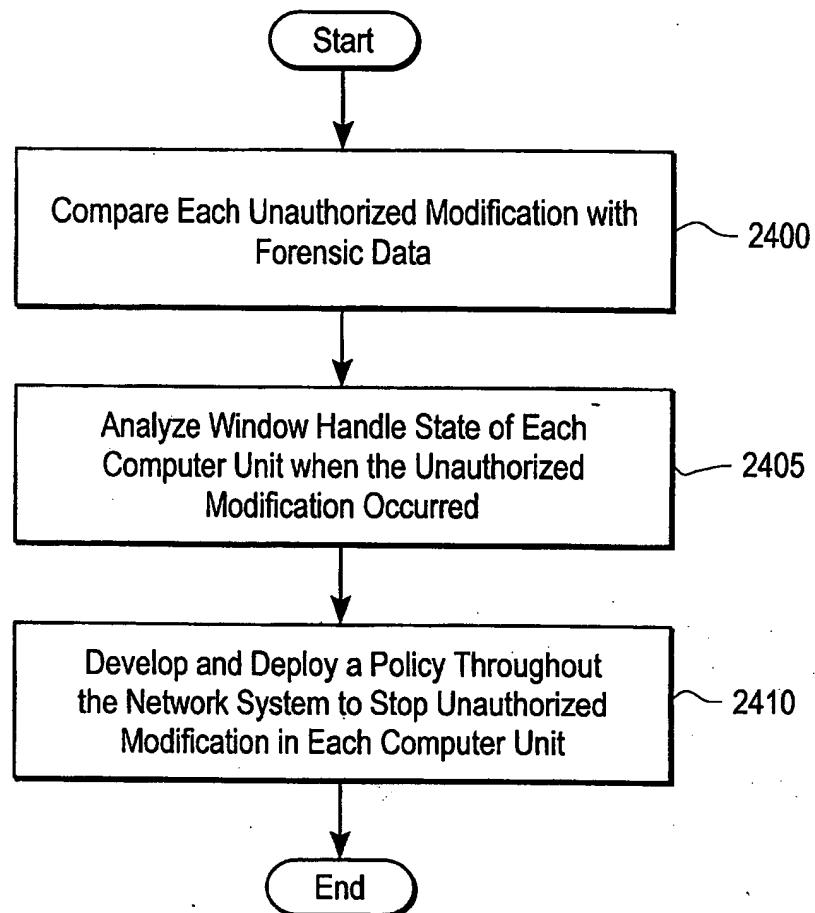


FIG. 24